



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

R. Kent
4/30/03
#26

In re application of : **Confirmation No. 2049**
Kazuto NISHIDA : Docket No.00177/526327
Serial No. 09/331,763 ✓ : Group Art Unit 3727
Filed June 25, 1999 : Examiner J. Merek

METHOD AND APPARATUS FOR MOUNTING
ELECTRONIC COMPONENT ON CIRCUIT BOARD

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEE FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975.

RESPONSE TO RESTRICTION REQUIREMENT

Assistant Commissioner for Patents,
Washington, D.C.

RECEIVED
APR 30 2003

Sir:

TECHNOLOGY CENTER R3700

This is in response to the Restriction Requirement of March 26, 2003.

Applicants, by their undersigned representative, hereby elect the invention of Group I which is drawn to a method of mounting an electronic component, and represented by claims 39-57, 77-79, 83-86 and 88-92.

This election is made with traverse since the apparatus claims of Group II have already been examined such that no additional burden would be placed on the Examiner by continuing to examine the claims of Group II.

Having made the required election, a full examination on the merits of the elected invention is hereby requested.

Respectfully submitted,

Kazuto NISHIDA

By: Joseph M. Gorski

Joseph M. Gorski
Registration No. 46,500
Attorney for Applicant

JMG/abm
Washington, D.C. 20006-1021
Telephone (202) 721-8200
Facsimile (202) 721-8250
April 28, 2003